
Macro Library Guide, June 1998

Addenda and Errata



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Introduction

This manual contains additions and corrections for the *Macro Library Guide June 1998*.

Additions

The following macros have been added in alphabetical order to the Input/Output section:

- CLKBUF1
- CLOCKINT1

The following macros have been added in alphabetical order to the Combinational section:

- CMA9
- CMB3
- CMEA
- CMF1
- CMF2
- CMF4
- CMF8
- CMAF

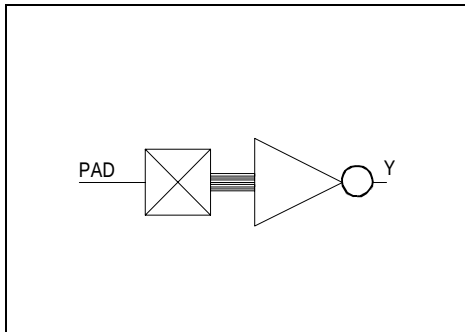
Corrections

The following macros replace the macros of the same name in the *Macro Library Guide June 1998*.

- AX02 - p. 36
- AX05 - p. 37
- MIN3X - p. 147

CLKBUFI

54SX

**Function**

Inverting Input for Dedicated Routed Clock Network

Truth Table

PAD	Y
0	1
1	0

Input	Output
PAD	Y

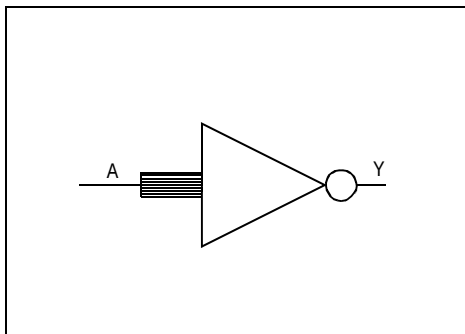
Family	Modules	
	Seq	I/O
54SX		1

NOTE 1: For an internal Clock net, refer to the CLKINTI macro.

NOTE 2: Refer to Actel's Databook for more Clock Network information.

CLKINTI

54SX

**Function**

Inverting Internal Clock Interface

Truth Table

A	Y
0	1
1	0

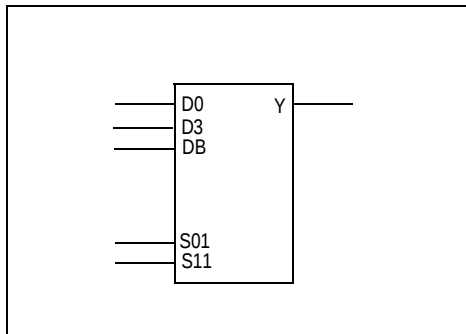
Input	Output
A	Y

NOTE: CLKINTI does not use any modules.

For more information on the Global Clock Network, refer to Actel's Databook.

CMA9

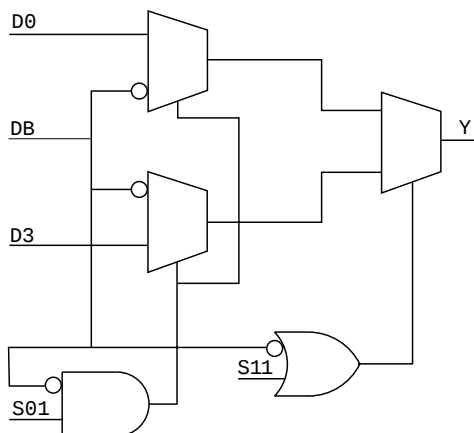
54SX



Input	Output
D0, D3, DB, S01, S11	Y

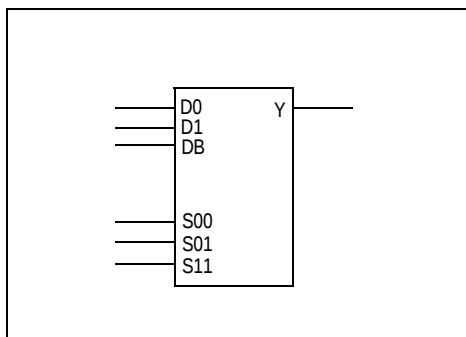
Family	Modules	
	Seq	Comb
54SX		1

Function
Full Combinational Module



CMB3

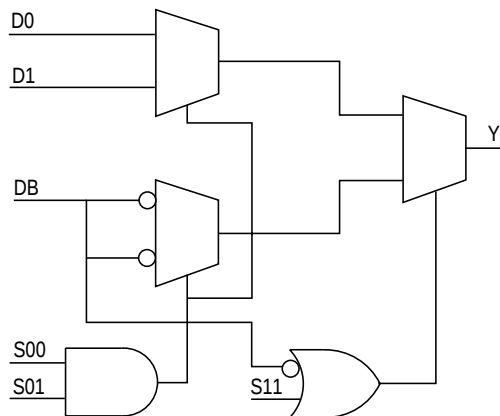
54SX



Input	Output
D0, D1, DB, S00, S01, S11	Y

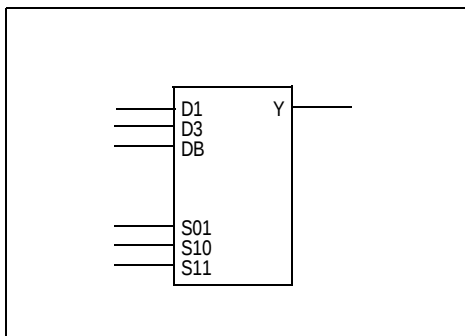
Family	Modules	
	Seq	Comb
54SX		1

Function
Full Combinational Module



CMEA

54SX

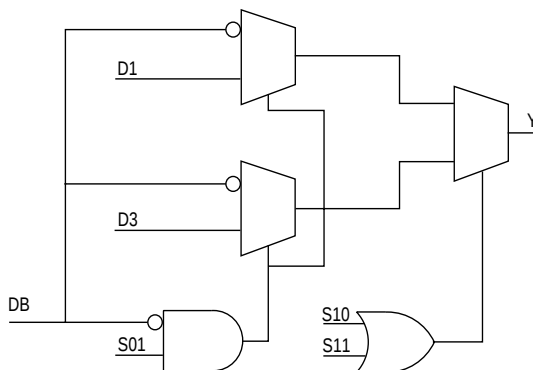


Input
D1, D3, DB, S01, S10, S11

Output
Y

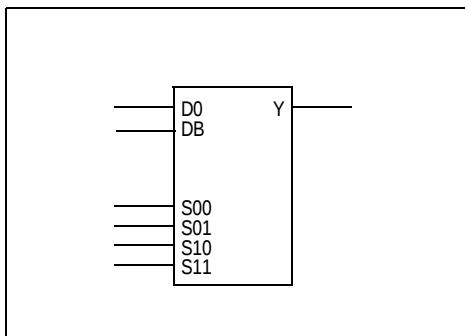
Family	Modules	
	Seq	Comb
54SX		1

Function
Full Combinational Module



CMF1

54SX

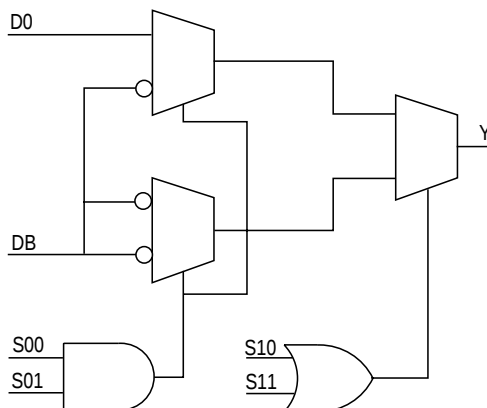


Input
D0, DB, S00, S01, S10, S11

Output
Y

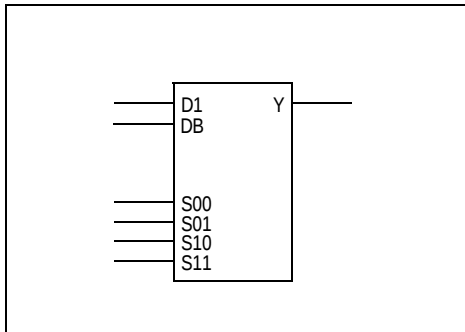
Family	Modules	
	Seq	Comb
54SX		1

Function
Full Combinational Module



CMF2

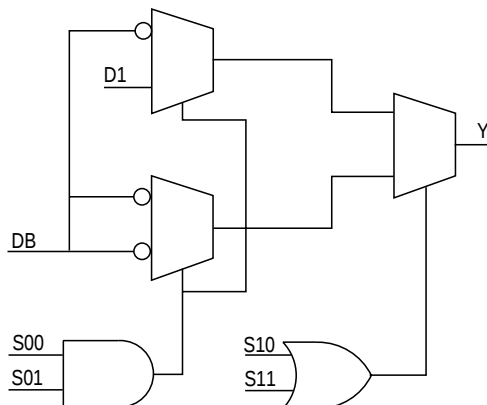
54SX



Input	Output
D1, DB, S00, S01, S10, S11	Y

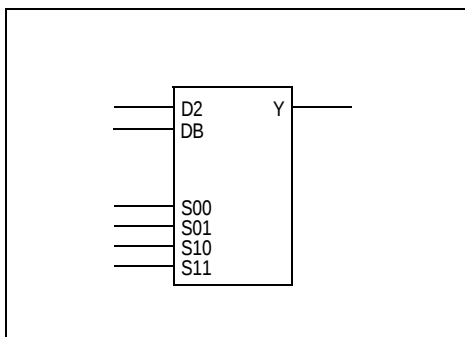
Family	Modules	
	Seq	Comb
54SX		1

Function
Full Combinational Module



CMF4

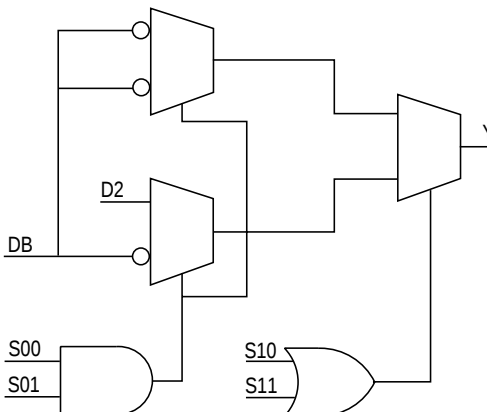
54SX



Input	Output
D2, DB, S00, S01, S10, S11	Y

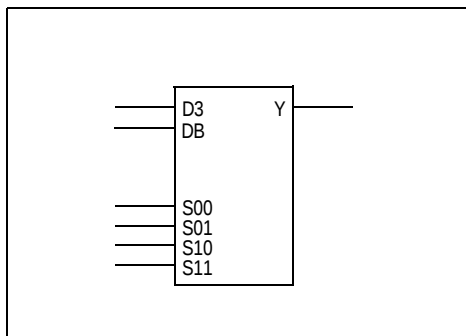
Family	Modules	
	Seq	Comb
54SX		1

Function
Full Combinational Module



CMF8

54SX



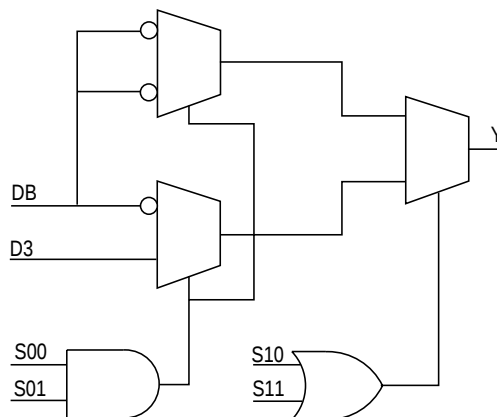
Input
D3, DB, S00, S01, S10, S11

Output
Y

Family	Modules	
	Seq	Comb
54SX		1

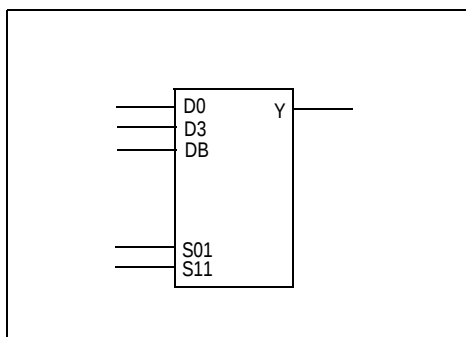
Function

Full Combinational Module



CMAF

54SX



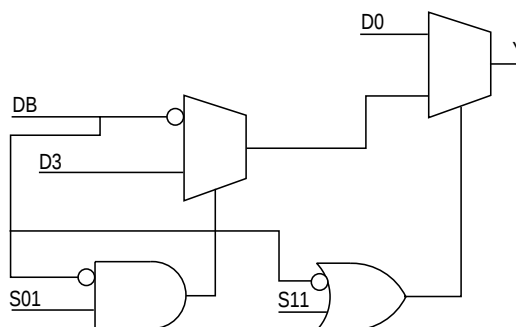
Input
D0, D3, DB, S01, S11

Output
Y

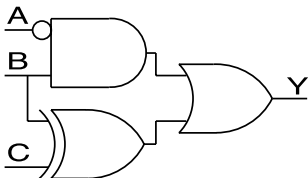
Family	Modules	
	Seq	Comb
54SX		1

Function

Full Combinational Module



Corrections for the June 1998 issue of the Macro Library Guide

AX02**54SX****Function**
3-Input Gate**Truth Table**

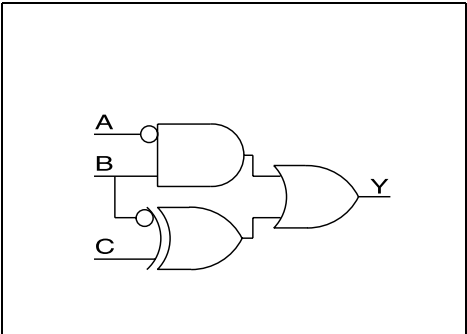
A	B	C	Y
0	0	0	0
1	0	0	0
0	1	0	1
1	1	0	1
0	0	1	1
1	0	1	1
0	1	1	1
1	1	1	0

Input
A, B, C**Output**
Y

Family	Modules	
	Seq	Comb
54SX		1

AXO5

54SX



Input A, B, C	Output Y
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Function

3-Input Gate

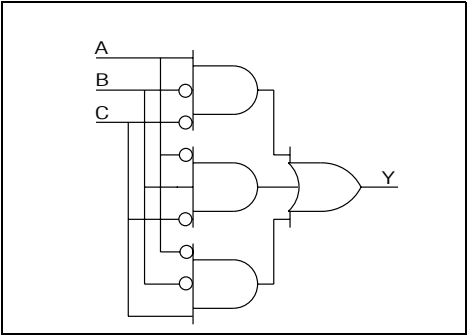
Truth Table

A	B	C	Y
0	0	0	1
1	0	0	1
0	1	0	1
1	1	0	0
0	0	1	0
1	0	1	0
0	1	1	1
1	1	1	1

Family	Modules	
	Seq	Comb
54SX		1

MIN3X

54SX



Input A, B, C	Output Y
-------------------------	--------------------

Function

1 of 3 function

Truth Table

A	B	C	Y
0	0	0	0
1	0	0	1
0	1	0	1
1	1	0	0
0	0	1	1
1	0	1	0
0	1	1	0
1	1	1	0

Family	Modules	
	Seq	Comb
54SX		1

